

Dr. NischayKumar Hegde. B.E., M. Tech., PhD.



meetnischay@gmail.com

+91-9481861701

<u>Career Objectives</u>	<i>To get established as a renowned teacher amongst the teaching fraternity by exhibiting my potential and abilities to meet the requirements of various stakeholders</i>
<u>Doctor of Philosophy</u>	<i>Ph. D (Computer Science & Engineering)</i> under CSE Dept., VTU, Belagavi Guide: Dr. Linganagouda Kulkarni Thesis Title: Cryptographic Schemes for Data Survivability for Unattended Wireless Sensor Networks Defended on: 24/11/2023
<u>Master Degree</u>	M. Tech. (Computer Science & Engineering)
Year of Passing	2011
Class	FCD, 8.35 CGPA.
Name of the Institution	NMAMIT, Nitte.
University	Visvesvaraya Technological University, Belagavi Thesis Title: Stream Cipher System Based on Multiple Parallel Binary Key Sequence Generators to Foil Side Channel Attack
<u>Bachelor Degree</u>	B.E. (Computer Science & Engineering)
Year of Passing	2005

Class	First Class with 65 % Aggregate.
Name of the Institution	Anjuman Engineering. College, Bhatkal.
University	Visvesvaraya Technological University, Belagavi. Project Title: IVRS for Academic Results Announcements
<u>Work Experience</u>	Total 20 Years of Teaching Experience.
<u>Currently Working</u>	
<u>Previously Working</u>	<ol style="list-style-type: none"> 1. February 2024 –February 2025: Associate Professor and Head, Vivekananda College of Engineering and Technology, Puttur 2. November 2021 – February 2024: Assistant Professor, Canara Engineering College, Benjanapadavu 3. August 2021 – October 2021: Assistant Professor, PES University, Electronic City Campus, Bangalore 4. December 2019 – July 2021: At home taking care of my mother, she was parallelized. 5. June 2013 – November 2019: Assistant Professor, Vivekananda College of Engineering & Technology [VCET], Puttur. 6. January 2012 – June 2013: Assistant Professor, P. A. College of Engineering, Mangalore 7. March 2008 – December 2011: Senior Lecturer, P. A. College of Engineering, Mangalore 8. August 2005 – February 2008: Lecturer, Anjuman Engineering College, Bhatkal
<u>Job Responsibilities</u>	<ol style="list-style-type: none"> 1. Coordinator for Time-Table 2. Coordinator for conduction of internal assessment Tests 3. Maintenance of Attendance and marks records of the students. 4. Lab set-up & up-gradation of all the Labs of CSE Departments 5. Coordinator for preparing different Lab Manuals 6. Coordinator for preparing Viva questions for the concerned Labs 7. Coordinator for organizing National Level Conferences,
<u>Administrative Works</u>	

	Workshops and Seminars/Technical Talks on latest Technologies
	8. Coordinator for Organizing Short term courses like Computer Basics, Improvising Presentation skills, Personality Development for students
Subjects Handled	9. Coordinator for organizing Graduation Day Celebrations (Institute Level)
	10. Institute Coordinator for VTU/ AICTE related matters 11. Coordinated for Institute Image Building in branding the organization
	Programming in C and Data Structures, Data Structures with C/C++, Computer Organization, Object Oriented Programming, Design and Analysis of Algorithms, Operating Systems, Software Engineering, Python Application Programming, Computer Networks, Java and J2EE, Object Oriented Modelling and Design, Internet of Things, Cryptographic and Network Security.
<u>Paper Publications – International Journals</u>	[1] Nischaykumar Hegde, Sharath K R, “Cardless ATM Cash Withdrawal: A Simple and Alternate Approach”, International Journal of Computer Science and Information Technology, ISSN: 0975-9646, Vol. 7 (1) , 2016, 126-128 [2] Nischaykumar Hegde, Pramodkumar P M, “ Improvising Round Robin Process Scheduling Through Dynamic Time Quantum Estimation”, International Journal of Engineering Research and General Science, ISSN 2091-2730, Vol. 4 (1), 2016, 339-345
<u>Paper Presentations – National Conferences</u>	[1] Nischaykumar Hegde, Radhakrishna Doddamane, Dr. Ganesh Aithal, “Multiple Parallel Linear Feedback Registers based Stream Cipher System to foil Side Channel Attack”, National Conference on Modern Trends in Science and Technology, 14 th – 15 th October 2011. [2] Nischaykumar Hegde, Srinivas Kattimani, Vishwakeerthi Vadya,

	<p>“Linear Feedback Registers based Random Number Generator”, UGC sponsored National Seminar on Research and Higher Education in Computer Science & Technology, 17th December 2014</p>
<p><u>Paper Presentations</u> –International Conferences</p>	<p>[1] Nischaykumar Hegde, Dr. Linganagouda Kulkarni, “Exploring Critical Factors Affecting WSN in the Context of Data Survivability”, International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), 28th – 29th January, 2018</p> <p>[2]] Nischaykumar Hegde, Dr. Linganagouda Kulkarni, “An Optimal Cryptographic Approach for Addressing Security Breaches to Build Resilient WSN”, The 3rd International Conference on Information and Communication Technology for Intelligent Systems, 6th – 7th April, 2018</p>
<p><u>Research Publications</u></p>	<p>[1] Nischaykumar Hegde, Dr. Linganagouda Kulkarni, “Exploring Critical Factors Affecting WSN in the Context of Survivability”, International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), Page 161 of 437978-1-5386-4304-4/18/\$31.00 ©2018 IEEE.</p> <p>[2] Nischaykumar Hegde, Dr. Linganagouda Kulkarni, “An Optimal Cryptographic Approach for Addressing Security Breaches to Build Resilient WSN”, The 3rd International Conference on Information and Communication Technology for Intelligent Systems, 2190-3018/18/19€ ©2018 Springer</p> <p>[3] Hegde Nischaykumar, Kulkarni Linganagouda, “A Smart Cryptographic Measure for Data Survivability in Unattended Wireless Sensor Networks” , International Journal of Innovative Technology and Exploring Engineering (IJITEE), ISSN: 2278–3075 (Online), Volume-9 Issue-3, January 2020, Page 3527-3530</p> <p>[4] Nischaykumar Hegde, Linganagouda Kulkarni, “Energy</p>

Aware Compressive Sensing Assisted Secure Data Survivability and Data Collection in Unattended Wireless Sensor Networks”, International Journal of Intelligent Engineering and Systems, Vol.15, No.4, 2022, DOI: 10.22266/ijies2022.0831.06

[5] Nischaykumar Hegde, Linganagouda Kulkarni , “Data Survivability in Unattended Wireless Sensor Networks with Optimal Clustering and Hybrid Encryption Algorithm”, International Journal of Wireless and Mobile Computing, 2024 DOI: 10.1504/IJWMC.2023.10061846

[6] Nischaykumar Hegde, Jagadisha N, M S Sathisha, Optimizing Zero-Trust Security with Multi-Agent Deep Q-Network for Dynamic Access Control, IEEE Transactions on Dependable and Secure Computing

Text Books
Publications

Python Programming Fundamentals – A Beginner’s Handbook, Educreation Publishing, ISBN: 978-1-5457-1355-6

Reference Book at

A] INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, RANCHI

<https://api.iiitranchi.ac.in/s1/uploads/045f5bd933/new-syllabus-cse-2020-onwards.pdf>

B] SNS College of Technology, COIMBATORE

<https://main.snsct.org/wp-content/uploads/2021/06/R2019-BE.CSE-CBCS-Curriculum-and-Syllabus.pdf>

C] Gujarat Technological University, Gujarat

<http://www.aitindia.in/aitnew/syllabus/civil/semester5/3151108.pdf>

D] Savitribai Phule Pune University (Formerly University of Pune)

<http://www.unipune.ac.in/dot/pdf-files/PGDDSandAI.pdf>

E] OP Jindal University, Raigarh

http://cloud.opju.ac.in/opju/menuitems/syllabus/1styears/Syllabus_B.Tech%201st%20Year.pdf

F] Ahmedabad Institute of Technology, Ahmedabad

<http://www.aitindia.in/aitnew/syllabus/civil/semester5/3151108.pdf>

G] CVM University, Gujarat

<https://www.mbit.edu.in/wp->

[content/uploads/2023/02/102040522_Python-for-Engineers.pdf](https://www.cbit.ac.in/wp-content/uploads/2023/02/102040522_Python-for-Engineers.pdf)

H] Chaitanya Bharathi Institute Of Technology, Telangana

[https://www.cbit.ac.in/wp-](https://www.cbit.ac.in/wp-content/uploads/2023/10/MCA_R23_Schema-Syllabus-I-TO-IV-SEMS-30-09-2023.pdf)

[content/uploads/2023/10/MCA_R23_Schema-Syllabus-I-TO-IV-SEMS-30-09-2023.pdf](https://www.cbit.ac.in/wp-content/uploads/2023/10/MCA_R23_Schema-Syllabus-I-TO-IV-SEMS-30-09-2023.pdf)

Data Structures and Algorithms, Scientific International Publishing House, Registered under the ministry of SME, ISBN: 978-93-5625-166-3

Conferences / FDP / Seminars / Workshops Organized

- [1] Organized a 3 Days Faculty Development Program on “Inculcating Best Research Practices”, 22 – 24 August, 2024, VCET, Puttur
- [2] Faculty Recharge Program on “Python Applications and Programming” on 29th January, 2018 at VCET, Puttur (D.K)
- [3] Guest Lecture on “Role of Sensors in IoT “, 16th February, 2018, at Vivekananda Degree College, Puttur (D.K)
- [4] National Conference on Signal and Image Processing at Vivekananda College of Engineering & Technology, Puttur, 1st & 2nd, May 2015
- [5] “Art of Programming”, Two days workshop at Vivekananda College of Engineering & Technology, Puttur, 12th & 13th, September 2015
- [6] Two days workshop on “Java and Pointers” Vivekananda College of Engineering & Technology, Puttur, July 2013
- [7] “Advanced Art of Programming & Angular JS”, Two days workshop at Vivekananda College of Engineering & Technology, Puttur, 6th & 7th, February 2015
- [8] Faculty Recharge Program on “Data Structures and Applications” on 1st August, 2017 at VCET, Puttur (D.K)
- [9] Guest Lecture on “Introduction to Wireless Sensor Networks “, 24th Friday,2015, at Vivekananda Degree College, Puttur (D.K)
- [10] Guest Lecture on “Networks and Network Security“, 16th July, 2014, at Vivekananda Degree College, Puttur (D.K)
- [11] Conducted Workshop on “Computer Basics” for Lab

	Instructors at the District Level
Conferences / FDP / Seminars / Workshops Attended	<p>[1] IEEE Sponsored International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC), 28th – 29th January, 2018</p> <p>[2] Springer Sponsored 3rd International Conference on Information and Communication Technology for Intelligent Systems, 6th – 7th April, 2018</p> <p>[3] UGC sponsored national seminar on Research & Higher Education in Computer Science & Technology, December 17 2014, Vivekananda College, Puttur</p> <p>[4] National Conference on “Current trends in Science and Technology” at Dr.MVSIT, Thodaru Moodabidri , 14th & 15th November, 2011.</p> <p>[5] Infosys Campus Connect, a Faculty Enablement Program at Infosys, Mysore, 27th June – 1st July, 2011</p> <p>[6] Two days workshop on “Latest Microsoft Technologies”, at SIT, Valachil, February 2011</p> <p>[7] Three days workshop on “College Automation Solution and University Management System”, at VTU Belagavi, 27th February – 1st March, 2007</p> <p>[8] Five days workshop on Microsoft.Net, at Anjuman Engineering College, Bhatkal, 4th – 8th November, 2006</p>
<u>Computer Knowledge</u>	
Operating Systems	DOS, Windows 2000, Windows NT/XP/7/8,10,11, Linux, Unix.
Programming Languages	C, C++, ASP.Net, Java, VB, PHP, Python, Arduino programming
RDBMS	MS SQL, MS Access & Oracle, MySql
<u>Certification Courses</u>	<p>Completed an On-line course on “Implementation of Data Structures”, sponsored by IIT, Bombay.</p> <p>On-line Certification course on “Programming, Data Structures and Algorithms using Python” sponsored by NPTEL</p> <p>On-line Certification course on “Software Engineering” sponsored by</p>

	NPTEL
<i>Nominations at Other Institutes</i>	<ul style="list-style-type: none"> • External Expert for Program Assessment and Quality Improvement Committee (PAQIC) Constitution of CSE(AI&ML) Department, SCEM, Mangalore • Member of BOE, MSc. Big Data Analytics, St. Agnes College (Autonomous) Mangalore • Reviewer for Journal of Computer Security, Q3 – Journal • Session Chair for ICT4SD 2025 : 10th International Conference on ICT for Sustainable Development, Goa, India
Membership Institution Organization	of / LMCSI, IAEC, LMISTE
Personal Details	
Name	Nischaykumar Hegde.
Father Name	Udaykumar Hegde
Date of Birth	05-03-1983
Sex	Male
Marital Status	Married
Nationality	Indian
Mother Tongue	Kannada
Languages Known	Kannada, English, Hindi.
Contact Numbers	+91-9481861701
Email IDs	meetnischay@gmail.com , meetnischay@rediffmail.com ,
Contact Address	S/o U M Hegde Near Mangala Enterprises Darandakukku, Puttur - 574203

Declaration: The above information provided is correct to the best of my knowledge and I shall be grateful if you provide me the chance to work with you. I shall work to best of my abilities and try to live up to your expectations.

Date: 18.06.2025

Nischaykumar Hegde